

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of :

Applicant : CHIH-FENG HUANG et al.

Filed : HEREWITH

For : ISOLATED HIGH-VOLTAGE LDMOS TRANSISTOR HAVING A  
SPLIT WELL STRUCTURE

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**INFORMATION DISCLOSURE STATEMENT**

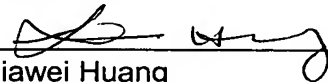
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing SIX ( 6 ) U.S. reference(s). This Information Disclosure Statement is being filed with this application, and no fee is required in accordance with 37 C.F.R. § 1.97(b)(1).

Respectfully submitted,

Dated: 1/16/2004

By:   
Jiawei Huang  
Registration No. 43,330

**Correspondence Address:**  
J.C. Patents  
4 Venture, Suite 250  
Irvine, California 92618  
Tel: (949) 660-0761

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE <b>INFORMATION DISCLOSURE STATEMENT BY          APPLICANT</b>	ATTY. DOCKET NO.: JCLA12651	APPLICATION No.: /
	APPLICANT: CHIH-FENG HUANG et al.	
	FILING DATE:	GROUP:

#### U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
		4,811,075	03/07/89	Eklund et al.	257	369	
		5,258,636	11/02/93	Rumenik et al.	257	339	
		6,617,652	09/09/03	Noda et al.	257	367	
		5,313,082	05/17/94	Eklund et al.	257	262	
		6,525,390	02/25/03	Tada et al.	257	489	
		6,570,219	05/27/03	Rumennik et al.	257	343	

#### FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION

#### OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

EXAMINER INITIAL		

EXAMINER

DATE CONSIDERED

**EXAMINER:** INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.